

**IN THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

1-22 (Canceled)

23. (Previously presented) A semiconductor device comprising:

a pixel region in which a plurality of thin film transistors are arranged in matrix;

a picture signal supply source for supplying a picture signal;

a switching circuit connected to a source signal line and a plurality of voltage lines for selecting at least one of said voltage lines;

a latch circuit for supplying said picture signal from said picture signal supply source to said switching circuit;

a memory for storing data used in gamma correction of a voltage applied to said at least one of said voltage lines; and

a gamma correction control circuit for adjusting said voltage based on said data to carry out said gamma correction of said voltage,

wherein said plurality of thin film transistors and said memory and said gamma correction control circuit are provided over a same insulating surface.

24. (Previously presented) A device according to claim 23 wherein said memory comprises a nonvolatile memory.

25. (Previously presented) A device according to claim 23 wherein said picture signal is a

digital signal.

26. (Previously presented) A device according to claim 23 wherein the picture signal is an analog signal, and the semiconductor device further comprises a conversion circuit for converting said analog signal to a digital signal.

27. (Previously presented) A device according to claim 23 wherein an active layer of each of said thin film transistors has a thickness of 10 to 100 nm.

28. (Previously presented) A device according to claim 23 wherein said semiconductor device is incorporated into one selected from the group consisting of a video camera, a still camera, a projector, a head mount display, a car navigation system, a personal computer, a portable information terminal, a mobile computer and a portable telephone.

29. (Previously presented) A device according to claim 23 further comprising a shift register.

30. (Previously presented) A device according to claim 23 wherein said memory comprises a thin film transistor.

31. (Previously presented) A device according to claim 23 wherein said gamma correction control circuit comprises a thin film transistor.

32. (Previously presented) A semiconductor device comprising:

an electroluminescence element;

a pixel region in which a plurality of thin film transistors are arranged in matrix;

a picture signal supply source for supplying a picture signal;

a switching circuit connected to a source signal line and a plurality of voltage lines for selecting at least one of said voltage lines;

a latch circuit for supplying said picture signal from said picture signal supply source to said switching circuit;

a memory for storing data used in gamma correction of a voltage applied to said at least one of said voltage lines; and

a gamma correction control circuit for adjusting said voltage based on said data to carry out said gamma correction of said voltage,

wherein said plurality of thin film transistors and said memory and said gamma correction control circuit are provided over a same insulating surface.

33. (Previously presented) A device according to claim 32 wherein said memory comprises a nonvolatile memory.

34. (Previously presented) A device according to claim 32 wherein said picture signal is a digital signal.

35. (Previously presented) A device according to claim 32 wherein the picture signal is an analog signal, and the semiconductor device further comprises a conversion circuit for converting said analog signal to a digital signal.

36. (Previously presented) A device according to claim 32 wherein an active layer of each of said thin film transistors has a thickness of 10 to 100 nm.

37. (Previously presented) A device according to claim 32 wherein said semiconductor device is incorporated into one selected from the group consisting of a video camera, a still camera, a projector, a head mount display, a car navigation system, a personal computer, a portable information terminal, a mobile computer and a portable telephone.

38. (Previously presented) A device according to claim 32 further comprising a shift register.

39. (Previously presented) A device according to claim 32 wherein said memory comprises a thin film transistor.

40. (Previously presented) A device according to claim 32 wherein said gamma correction control circuit comprises a thin film transistor.

41-50 (Canceled).

51. (Previously presented) A semiconductor device comprising:

an active layer provided over a substrate;

a source region and a drain region provided in said active layer; and

a floating gate provided adjacent to said active layer with a gate insulating film

therebetween,

wherein said source region and said drain region contain a P type impurity,

wherein mobility of said semiconductor device is 100 to 300 cm<sup>2</sup>/Vs, and

wherein said active layer has a thickness of 10 to 100 nm.

52. (Previously presented) A device according to claim 51 wherein said semiconductor device is incorporated into one selected from the group consisting of a video camera, a still camera, a projector, a head mount display, a car navigation system, a personal computer, a portable information terminal, a mobile computer and a portable telephone.

53. (Previously presented) A device according to claim 51 wherein said substrate comprises a material selected from the group consisting of quartz, silicon and ceramic.

54. (Previously presented) A device according to claim 51 wherein said active layer comprises silicon.

55. (Previously presented) A device according to claim 51 wherein said floating gate comprises a material selected from the group consisting of aluminum and silicon.

56. (Previously presented) A device according to claim 51 wherein said P type impurity comprises boron.

57. (Previously presented) A semiconductor device comprising:

an active layer provided over a substrate;  
a source region and a drain region provided in said active layer;  
a floating gate provided adjacent to said active layer with a gate insulating film therebetween; and  
an insulating film comprising an organic resin provided over said floating gate to provide a leveled upper surface over said floating gate,  
wherein said source region and said drain region contain a P type impurity,  
wherein mobility of said semiconductor device is 100 to 300 cm<sup>2</sup>/Vs, and  
wherein said active layer has a thickness of 10 to 100 nm.

58. (Previously presented) A device according to claim 57 wherein said semiconductor device is incorporated into one selected from the group consisting of a video camera, a still camera, a projector, a head mount display, a car navigation system, a personal computer, a portable information terminal, a mobile computer and a portable telephone.

59. (Previously presented) A device according to claim 57 wherein said substrate comprises a material selected from the group consisting of quartz, silicon and ceramic.

60. (Previously presented) A device according to claim 57 wherein said active layer comprises silicon.

61. (Previously presented) A device according to claim 57 wherein said floating gate comprises a material selected from the group consisting of aluminum and silicon.

62. (Previously presented) A device according to claim 57 wherein said P type impurity comprises boron.

63. (Previously presented) A device according to claim 57 wherein said organic resin comprises a material selected from the group consisting of polyimide, acryl, polyamide and polyimide amide.

64. (Previously presented) A semiconductor device comprising:

- a pixel region in which a plurality of thin film transistors are arranged in matrix;
- a picture signal supply source for supplying a picture signal;
- a switching circuit connected to a plurality of voltage lines for selecting at least one of said voltage lines;
- a latch circuit for supplying said picture signal from said picture signal supply source to said switching circuit;
- a gamma correction control circuit for adjusting a voltage applied to said at least one of said voltage lines,
- wherein said plurality of thin film transistors and said gamma correction control circuit are provided over a same insulating surface.

65. (Previously presented) A device according to claim 64 wherein said picture signal is a digital signal.

66. (Previously presented) A device according to claim 64 wherein the picture signal is an analog signal, and the semiconductor device further comprises a conversion circuit for converting said analog signal to a digital signal.

67. (Previously presented) A device according to claim 64 wherein an active layer of each of said thin film transistors has a thickness of 10 to 100 nm.

68. (Previously presented) A device according to claim 64 wherein said semiconductor device is incorporated into one selected from the group consisting of a video camera, a still camera, a projector, a head mount display, a car navigation system, a personal computer, a portable information terminal, a mobile computer and a portable telephone.

69. (Previously presented) A device according to claim 64 further comprising a shift register.

70. (Previously presented) A device according to claim 64 wherein said gamma correction control circuit comprises a thin film transistor.

71. (Previously presented) A semiconductor device comprising:

- an electroluminescence element;
- a pixel region in which a plurality of thin film transistors are arranged in matrix;
- a picture signal supply source for supplying a picture signal;
- a switching circuit connected to a plurality of voltage lines for selecting at least one of said voltage lines;



a latch circuit for supplying said picture signal from said picture signal supply source to said switching circuit;

a gamma correction control circuit for adjusting a voltage applied to said at least one of said voltage lines,

wherein said plurality of thin film transistors and said gamma correction control circuit are provided over a same insulating surface.

72. (Previously presented) A device according to claim 71 wherein said picture signal is a digital signal.

73. (Previously presented) A device according to claim 71 wherein the picture signal is an analog signal, and the semiconductor device further comprises a conversion circuit for converting said analog signal to a digital signal.

74. (Previously presented) A device according to claim 71 wherein an active layer of each of said thin film transistors has a thickness of 10 to 100 nm.

75. (Previously presented) A device according to claim 71 wherein said semiconductor device is incorporated into one selected from the group consisting of a video camera, a still camera, a projector, a head mount display, a car navigation system, a personal computer, a portable information terminal, a mobile computer and a portable telephone.

76. (Previously presented) A device according to claim 71 further comprising a shift register.

77. (Previously presented) A device according to claim 71 wherein said gamma correction control circuit comprises a thin film transistor.

78. (Previously presented) A semiconductor device comprising:

- a pixel region in which a plurality of thin film transistors are arranged in matrix;

- a picture signal supply source for supplying a picture signal;

- a switching circuit connected to a plurality of voltage lines for selecting at least one of said voltage lines;

- a latch circuit for supplying said picture signal from said picture signal supply source to said switching circuit;

- a memory for storing data used in gamma correction of a voltage applied to said at least one of said voltage lines;

- a gamma correction control circuit for adjusting said voltage based on said data to carry out said gamma correction of said voltage,

wherein said plurality of thin film transistors and said memory and said gamma correction control circuit are provided over a same insulating surface.

79. (Previously presented) A device according to claim 78 wherein said memory comprises a nonvolatile memory.

80. (Previously presented) A device according to claim 78 wherein said picture signal is a digital signal.

81. (Previously presented) A device according to claim 78 wherein the picture signal is an analog signal, and the semiconductor device further comprises a conversion circuit for converting said analog signal to a digital signal.

82. (Previously presented) A device according to claim 78 wherein an active layer of each of said thin film transistors has a thickness of 10 to 100 nm.

83. (Previously presented) A device according to claim 78 wherein said semiconductor device is incorporated into one selected from the group consisting of a video camera, a still camera, a projector, a head mount display, a car navigation system, a personal computer, a portable information terminal, a mobile computer and a portable telephone.

84. (Previously presented) A device according to claim 78 further comprising a shift register.

85. (Previously presented) A device according to claim 78 wherein said memory comprises a thin film transistor.

86. (Previously presented) A device according to claim 78 wherein said gamma correction control circuit comprises a thin film transistor.

87. (Previously presented) A semiconductor device comprising:

an electroluminescence element;

a pixel region in which a plurality of thin film transistors are arranged in matrix;  
a picture signal supply source for supplying a picture signal;  
a switching circuit connected to a plurality of voltage lines for selecting at least one of said voltage lines;  
a latch circuit for supplying said picture signal from said picture signal supply source to said switching circuit;  
a memory for storing data used in gamma correction of a voltage applied to said at least one of said voltage lines;  
a gamma correction control circuit for adjusting said voltage based on said data to carry out said gamma correction of said voltage,  
wherein said plurality of thin film transistors and said memory and said gamma correction control circuit are provided over a same insulating surface.

88. (Previously presented) A device according to claim 87 wherein said memory comprises a nonvolatile memory.

89. (Previously presented) A device according to claim 87 wherein said picture signal is a digital signal.

90. (Previously presented) A device according to claim 87 wherein the picture signal is an analog signal, and the semiconductor device further comprises a conversion circuit for converting said analog signal to a digital signal.

91. (Previously presented) A device according to claim 87 wherein an active layer of each of said thin film transistors has a thickness of 10 to 100 nm.

92. (Previously presented) A device according to claim 87 wherein said semiconductor device is incorporated into one selected from the group consisting of a video camera, a still camera, a projector, a head mount display, a car navigation system, a personal computer, a portable information terminal, a mobile computer and a portable telephone.

93. (Previously presented) A device according to claim 87 further comprising a shift register.

94. (Previously presented) A device according to claim 87 wherein said memory comprises a thin film transistor.

95. (Previously presented) A device according to claim 87 wherein said gamma correction control circuit comprises a thin film transistor.

96. (New) A semiconductor device according to claim 23 wherein said switching circuit comprises a thin film transistor.

97. (New) A semiconductor device according to claim 23 wherein said latch circuit comprises a thin film transistor.

98. (New) A semiconductor device according to claim 32 wherein said switching circuit

comprises a thin film transistor.

99. (New) A semiconductor device according to claim 32 wherein said latch circuit comprises a thin film transistor.

100. (New) A semiconductor device according to claim 64 wherein said switching circuit comprises a thin film transistor.

101. (New) A semiconductor device according to claim 64 wherein said latch circuit comprises a thin film transistor.

102. (New) A semiconductor device according to claim 71 wherein said switching circuit comprises a thin film transistor.

103. (New) A semiconductor device according to claim 71 wherein said latch circuit comprises a thin film transistor.

104. (New) A semiconductor device according to claim 78 wherein said switching circuit comprises a thin film transistor.

105. (New) A semiconductor device according to claim 78 wherein said latch circuit comprises a thin film transistor.

106. (New) A semiconductor device according to claim 87 wherein said switching circuit comprises a thin film transistor.

107. (New) A semiconductor device according to claim 87 wherein said latch circuit comprises a thin film transistor.